



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): NOGUCHI et al.

Serial No.: 09/825,946

Filed: April 5, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND

FABRICATION METHOD FOR SEMICONDUCTOR INTEGRATED

March 7, 2002

CIRCUIT DEVICE

Group: 2811

Examiner: H. Vu

## RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

In response to the restriction requirement dated February 7, 2002, applicants hereby elect the Group II, claims 20-45, without traverse.

If the Examiner believes that there are any other points which may be clarified or otherwise disposed of, either by telephone discussion or by personal interview, the Examiner is invited to contact applicants' undersigned attorney at the number indicated below.

It is respectfully requested that any shortage in the fee be charged to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (Case No. 501.39932X00).

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

Gregory E. Montone

Registration No. 28,141

GEM/kd 703/312-6600

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